

Receipt date: 03/14/2005

## INFORMATION DISCLOSURE CITATION

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10518556 - GAU: 2192

ATTY DOCKET NO.

5035-200 US

APPLICATION NO.

10/518,556

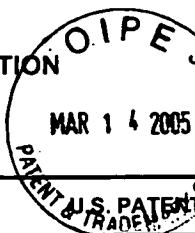
APPLICANT(S)

Richard Michael Taylor

FILING DATE

December 22, 2004

GROUP ART UNIT



EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A	5,507,030	9 April 1996	Sites, Richard L			

## U.S. PATENT APPLICATION PUBLICATIONS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	B	WO 01/59593 A2	16 Aug. 2001	PCT				

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	C	Henk Corporaal; "Design of Transport Triggered Architecture"; Delft University of Technology; 1994 IEEE; 1066-1395; pp 130 - 135
	D	Henk Corporaal; "A Different Approach to High Performance Computing"; Delft University of Technology;

EXAMINER /Michael Yigdal/

DATE CONSIDERED 08/01/2008

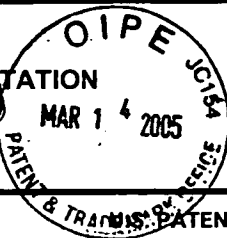
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							YES	NO

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)

		E	Werner Kreuser; Martin Gotschlich; Bernhard Wess; "A Retargetable Optimizing Code Generator for Digital Signal Processors"; 1996 IEEE; 0-7803-3073-0/96; pp 257 - 260

EXAMINER /Michael Yigdall/

DATE CONSIDERED 08/01/2008

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